

# Optimal Clock Synchronization in Networks

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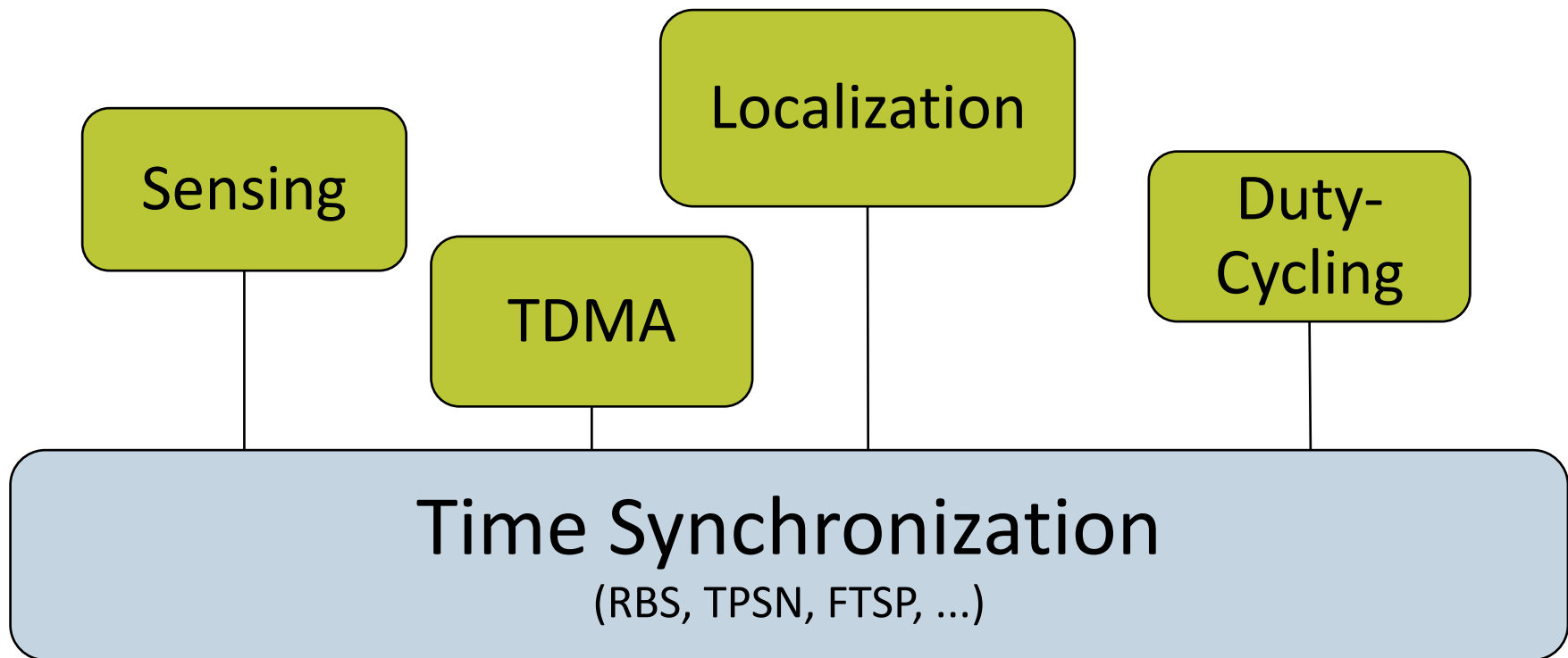
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**Distributed  
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# Time in Sensor Networks

- Synchronized clocks are essential for many applications:

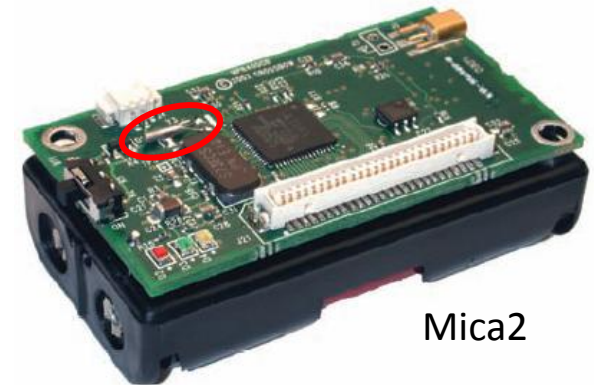


# Hardware Clocks Experience Drift

- Hardware clock

  - Counter register of the microcontroller

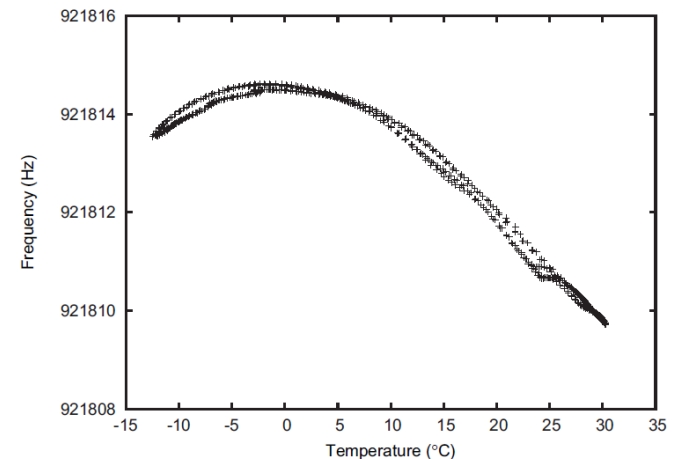
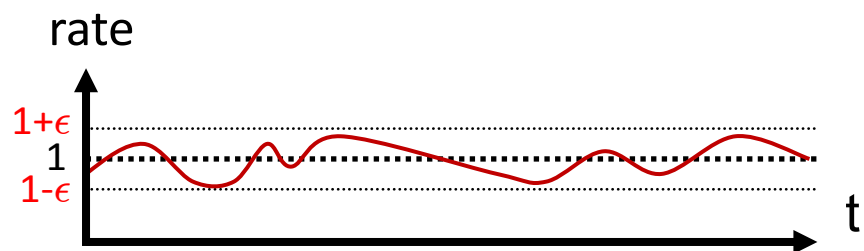
  - Sourced by an external crystal (32kHz, 7.37 MHz)



Mica2

- Clock drift

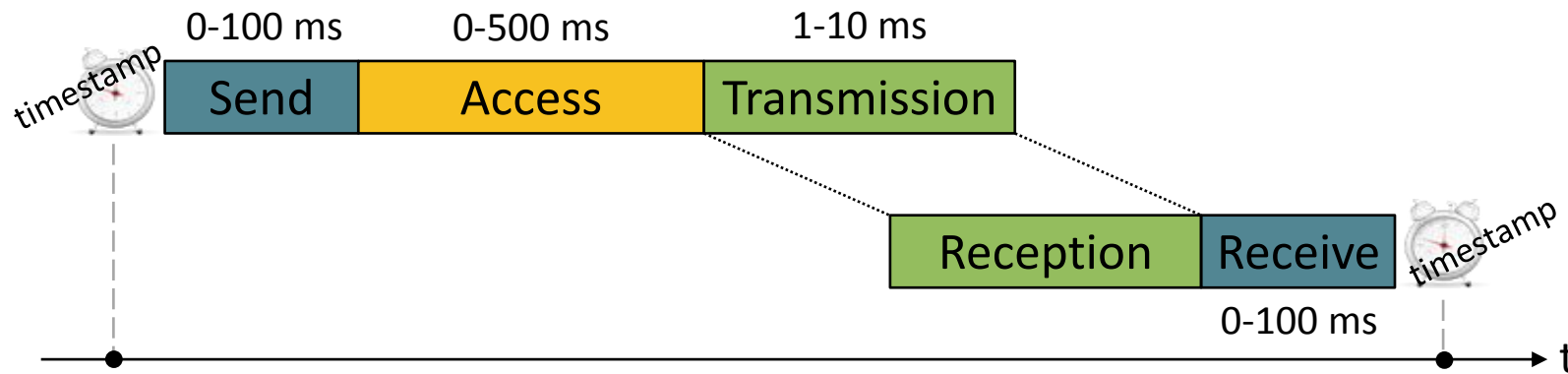
  - Random deviation from the nominal rate dependent on ambient temperature, power supply, etc. (30-100 ppm)



# Messages Experience Jitter in the Delay

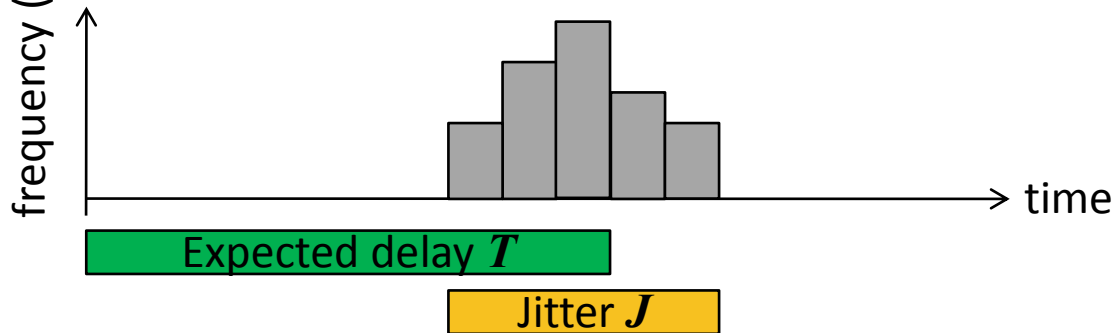
- Problem: Jitter in the message delay

Various sources of errors (deterministic and non-deterministic)



- Solution: Timestamping packets at the MAC layer (Maróti et al.)

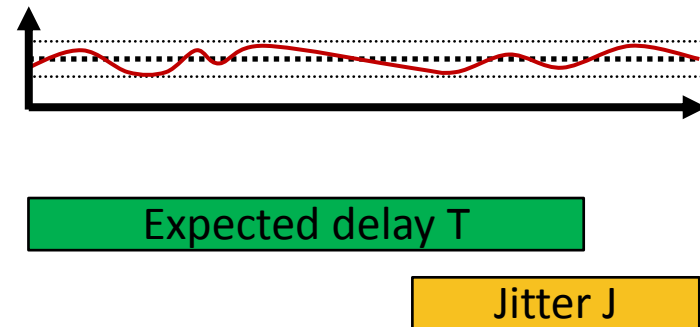
→ Jitter in the message delay is reduced to a few clock ticks





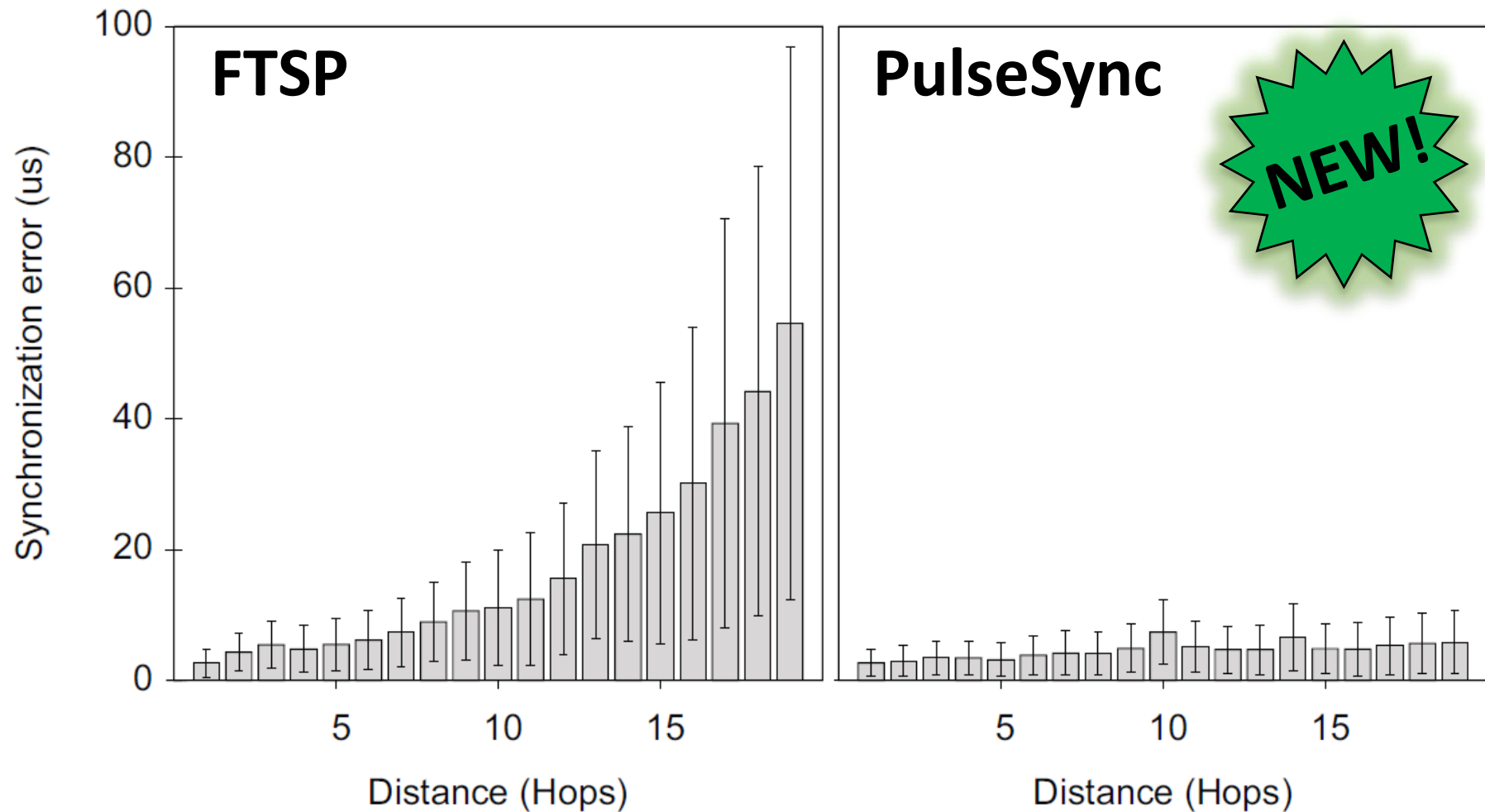
# Summary: Clock Synchronization

- Goal:
  - Send time information (beacons) to synchronize clocks
- Problems:
  - Hardware clocks exhibit **drift**
  - **Jitter** in the message delay



# Preview: Experimental Results

- Synchronization error vs. hop distance



A photograph of the Golden Gate Bridge in San Francisco, showing the suspension towers and the bridge deck over the water.

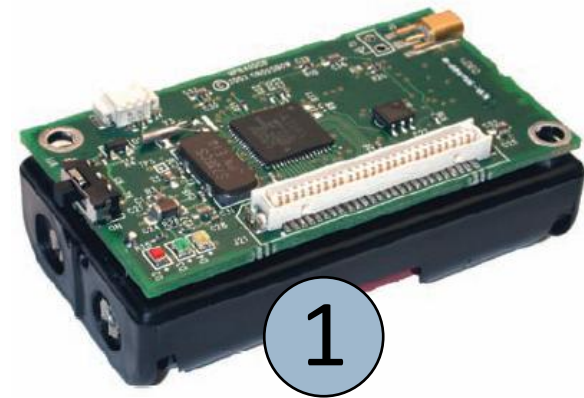
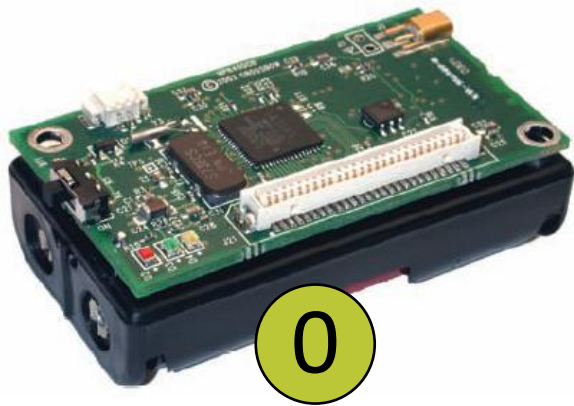
# Outline

- Introduction
- Theory
- Practice

# Synchronizing Nodes: Single-Hop

- How do we synchronize the clocks of **two** sensor nodes?

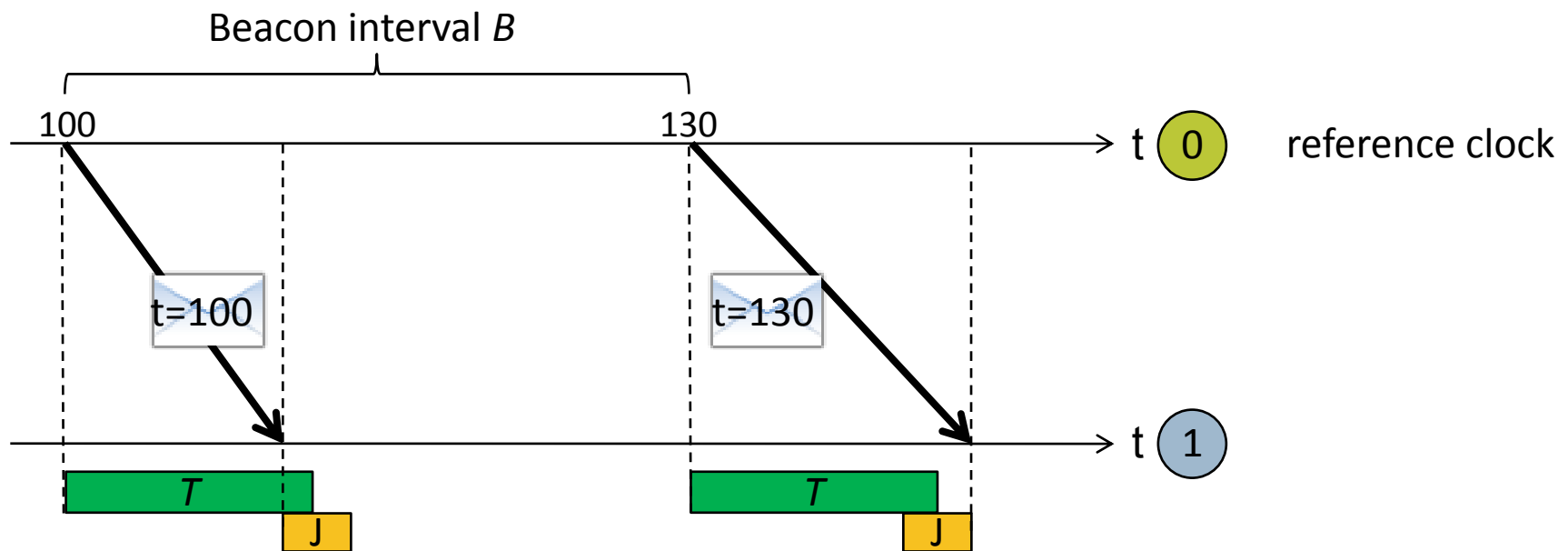
reference clock





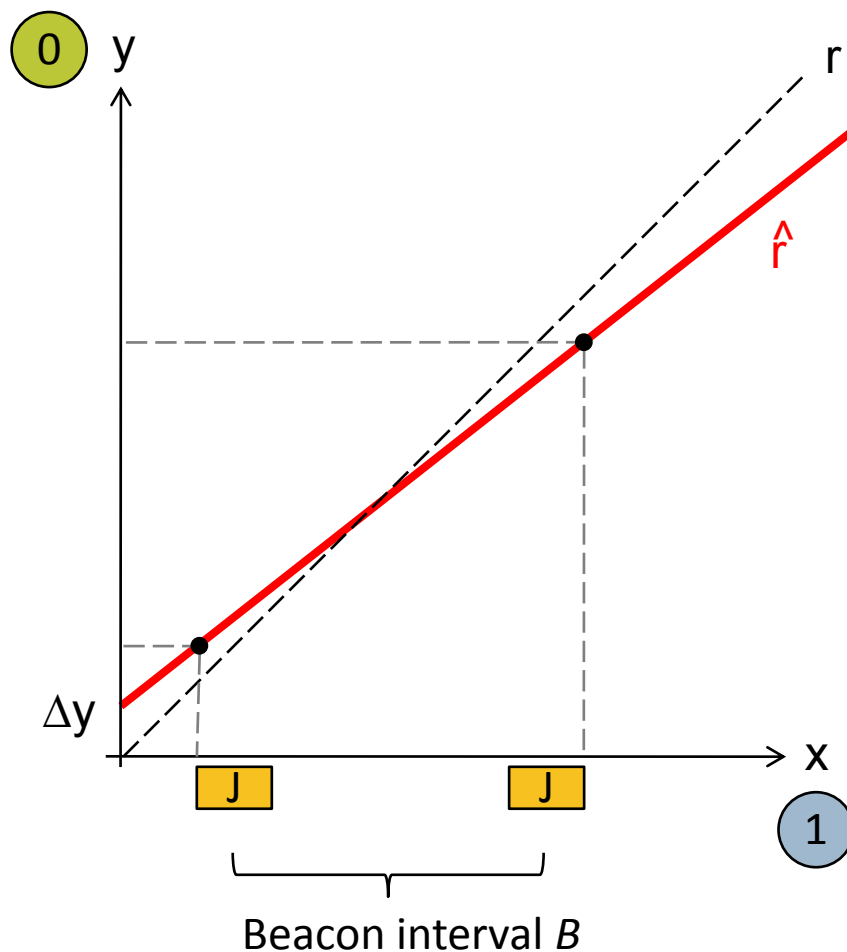
# Synchronizing Nodes

- Sending periodic beacons to synchronize nodes



# How accurately can we synchronize two Nodes?

- Message delay jitter affects clock synchronization quality

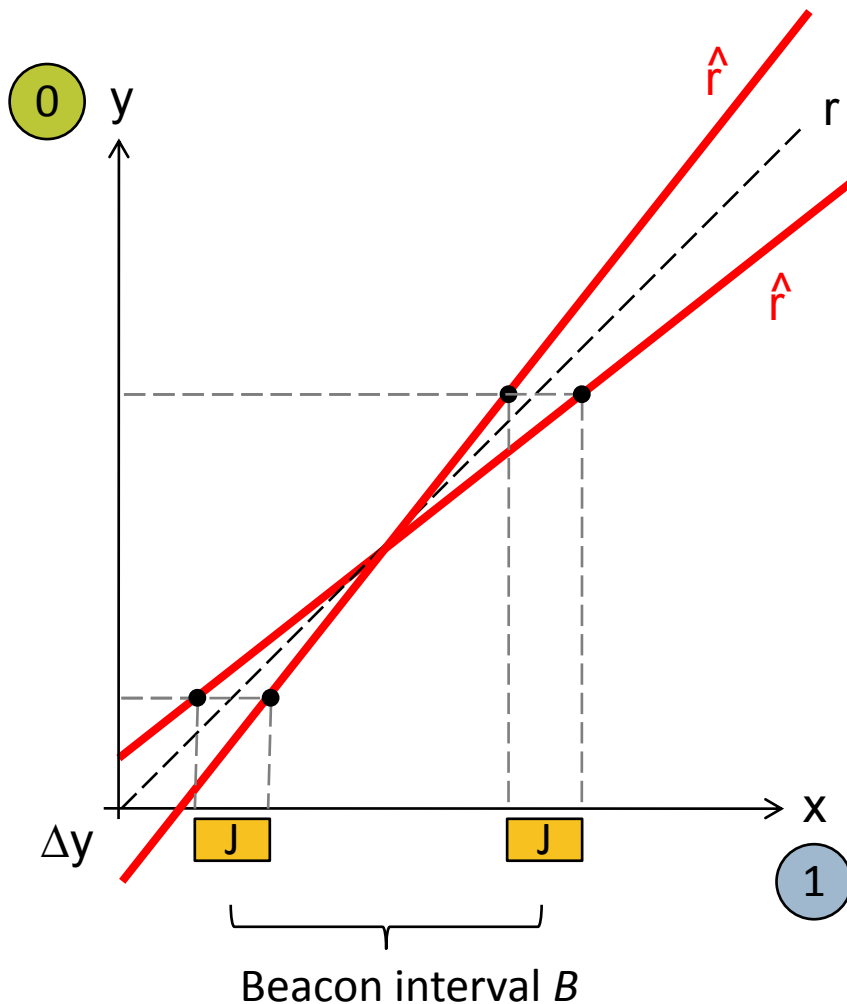


$$y(x) = \hat{r} \cdot x + \Delta y$$

↑ clock offset  
↑ relative clock rate (estimated)

# How accurately can we synchronize two Nodes?

- Message delay jitter affects clock synchronization quality

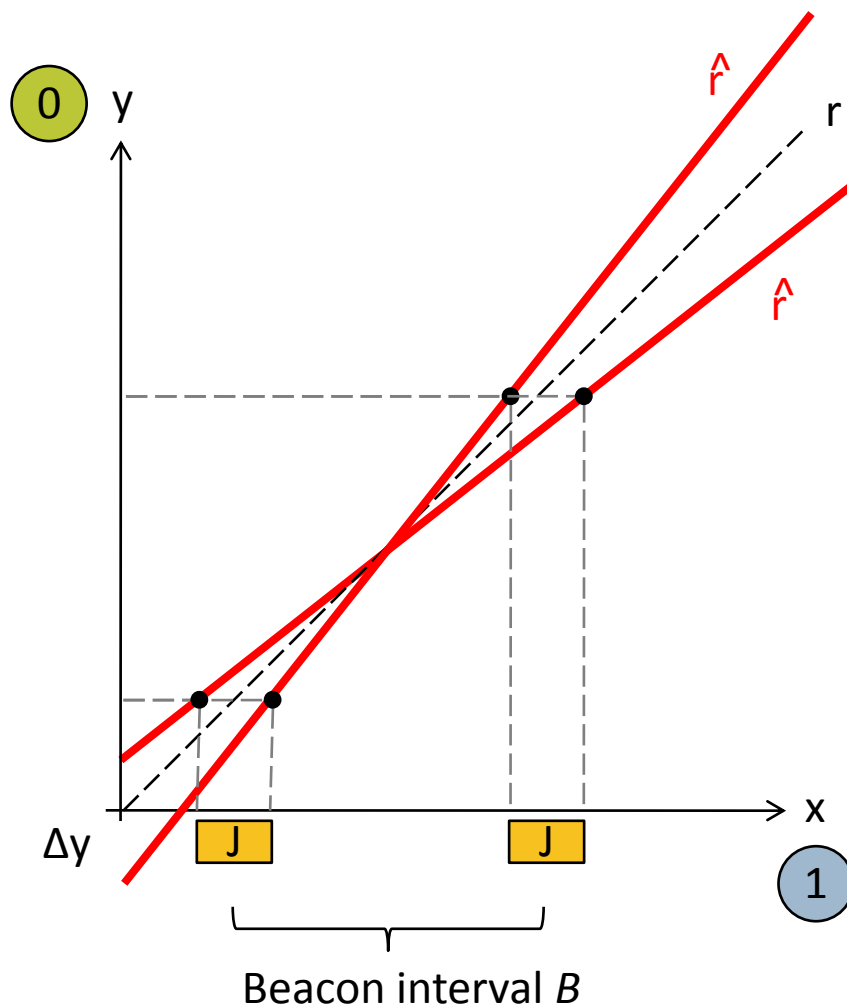


$$y(x) = \hat{r} \cdot x + \Delta y$$

↑ clock offset  
↑ relative clock rate (estimated)

# Clock Skew between two Nodes

- Lower Bound on the clock skew between two neighbors



Error in the rate estimation:

- Jitter in the message delay
- Beacon interval
- Number of beacons  $k$

$$|\hat{r} - r| \sim \frac{J}{Bk\sqrt{k}}$$

Synchronization error:

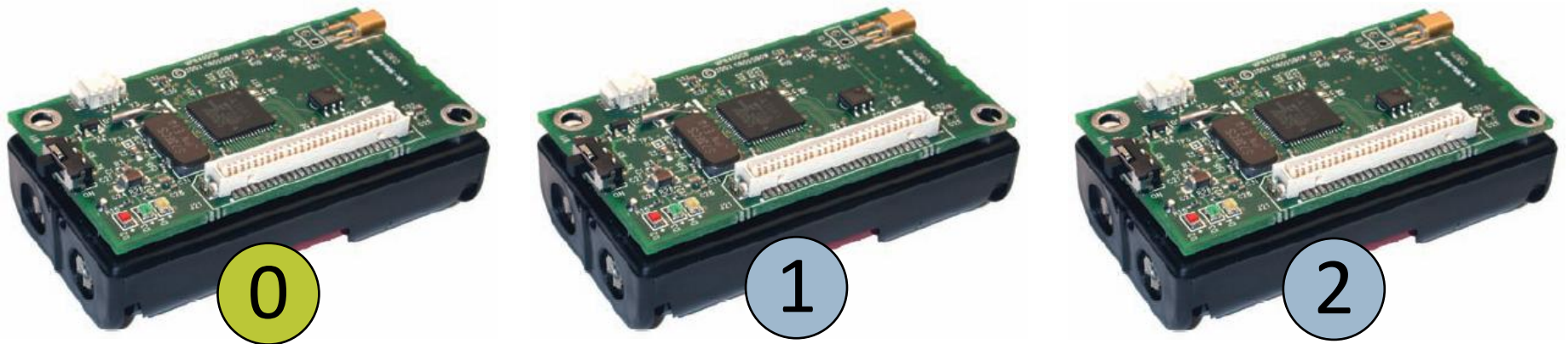
$$|\hat{y} - y| \sim \frac{J}{\sqrt{k}}$$

(complete proof is in the paper)

# Synchronizing Nodes: Multi-hop

- How do we synchronize the clocks of **multiple** sensor nodes?

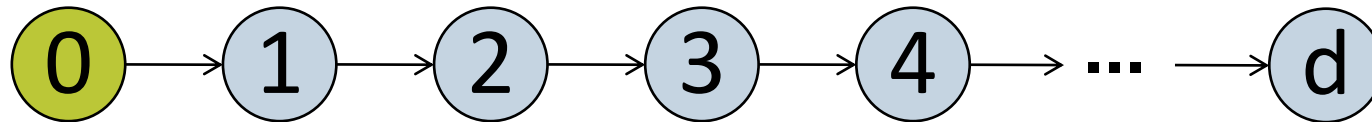
reference clock





# Now we have a network of nodes!

- How does the network diameter affect synchronization errors?



- Examples for sensor networks with high diameter

Bridge, road or pipeline monitoring

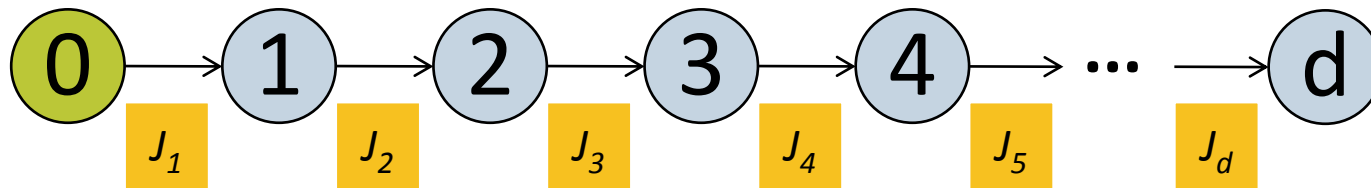


Deployment at Golden Gate Bridge with 46 hops  
(Kim et al., IPSN'07)

# Multi-hop Clock Synchronization

- Nodes forward their current estimate of the reference clock

Each synchronization beacon is affected by a **random jitter  $J$**



- Sum of the jitter grows with the square-root of the distance

$$\text{stddev}(J_1 + J_2 + J_3 + J_4 + J_5 + \dots J_d) = \sqrt{d} \times \text{stddev}(J)$$

Single-hop:

$$|\hat{y} - y| \sim \frac{J}{\sqrt{k}}$$



Multi-hop:

$$|\hat{y} - y| \sim \frac{J\sqrt{d}}{\sqrt{k}}$$

(proof is in the paper)

A photograph of the Golden Gate Bridge in San Francisco, showing the red-orange steel structure and the suspension cables against a blue sky and water.

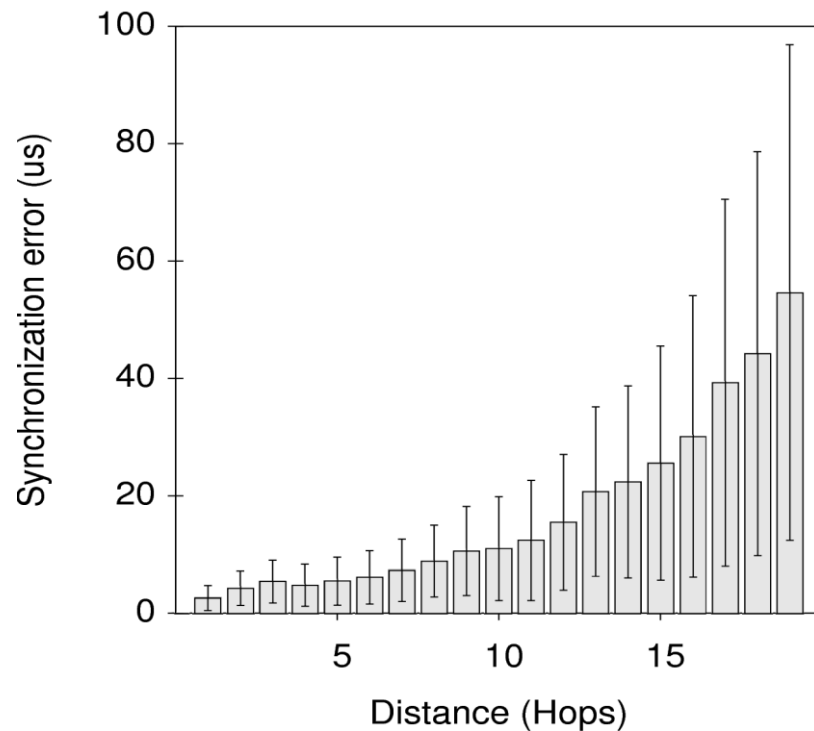
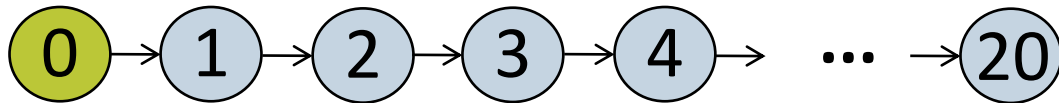
# Outline

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# Testbed Experiments (FTSP)

- Measurement results from testbed with 20 Mica2 nodes

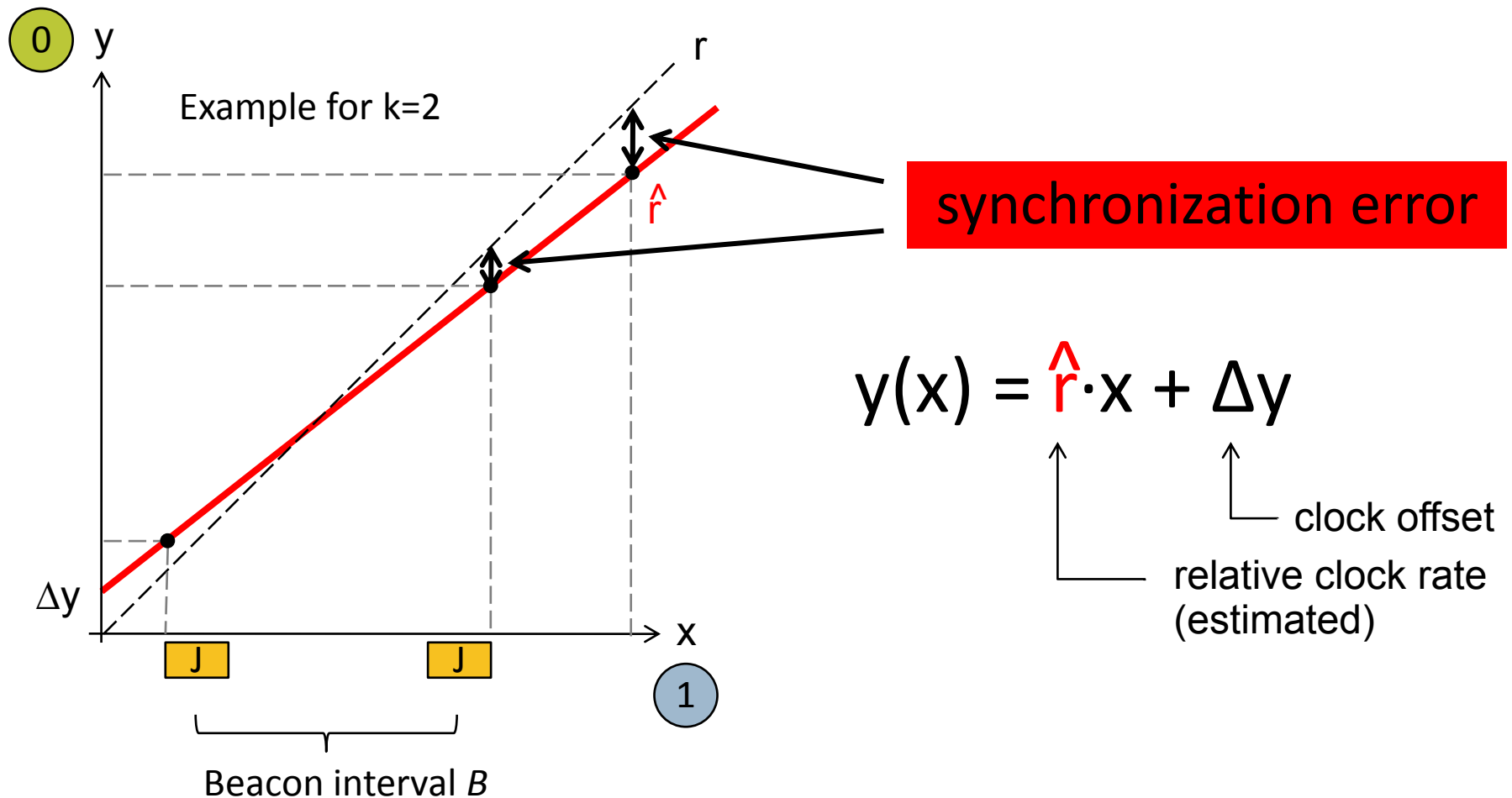


- Synchronization error grows **exponentially**
- Nodes far away from the root failed to synchronize with their parent node



# Linear Regression (FTSP)

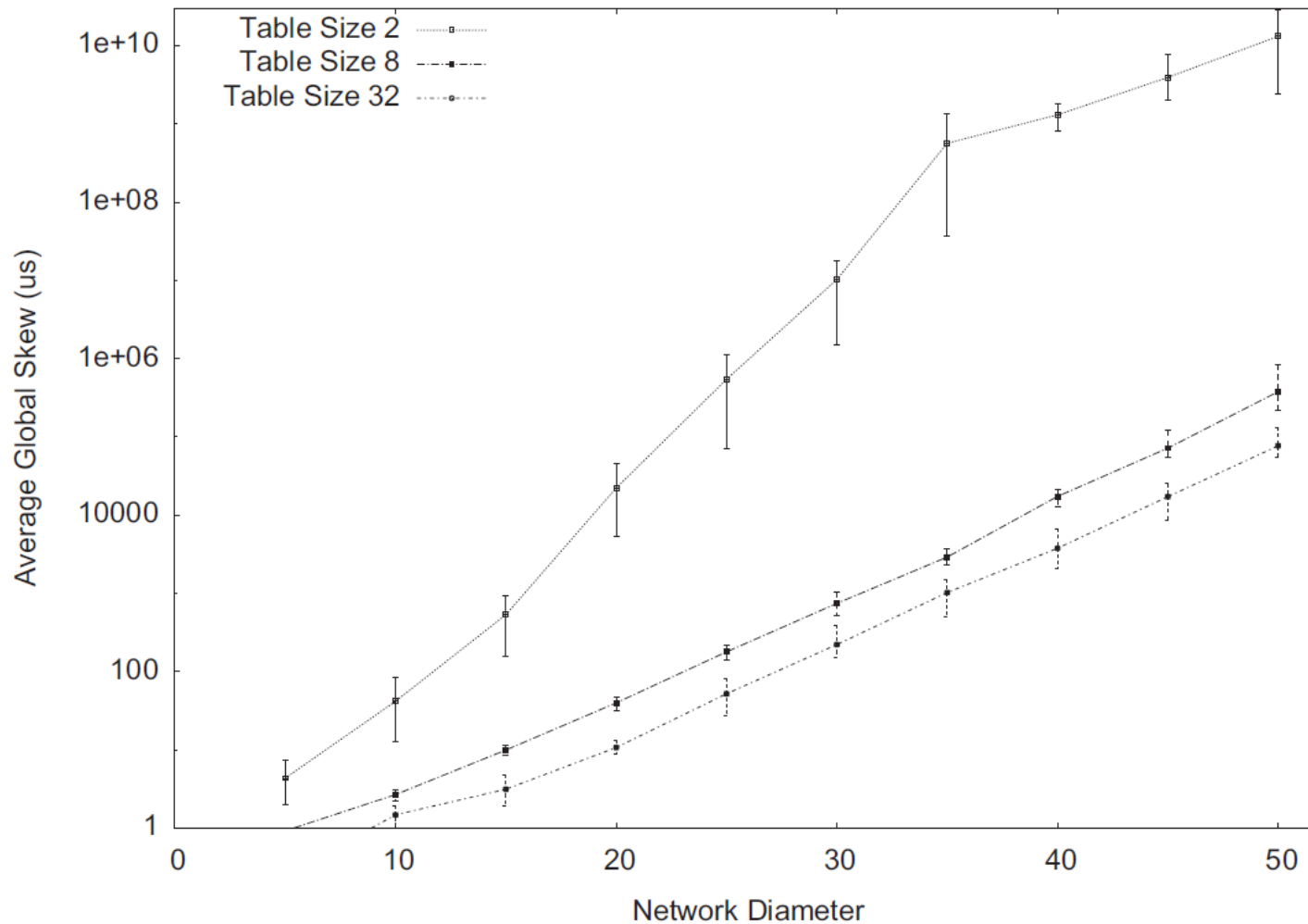
- FTSP uses linear regression to compensate for clock drift  
Jitter is amplified before it is sent to the next hop



# Linear Regression (FTSP)

- Simulation of FTSP with regression tables of different sizes ( $k = 2, 8, 32$ )

Log Scale!

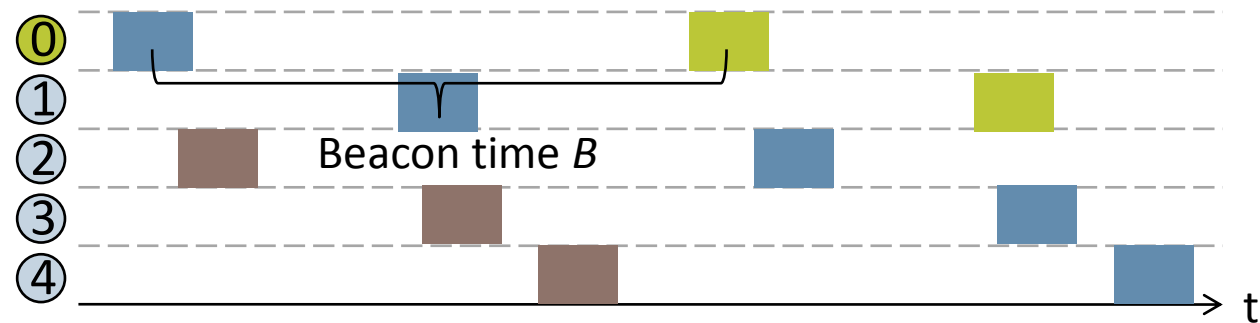


# The PulseSync Protocol

- Send fast synchronization pulses through the network
  - Speed-up the initialization phase
  - Faster adaptation to changes in temperature or network topology

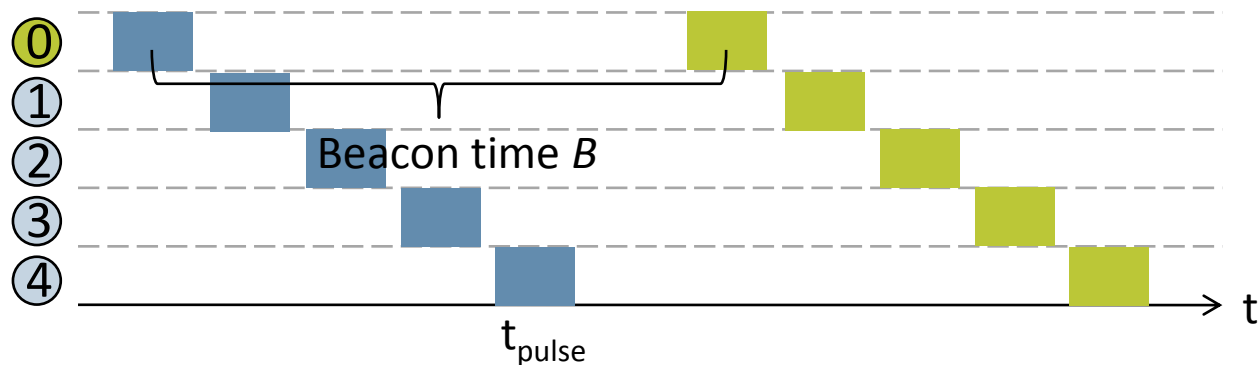
## FTSP

Expected time  
 $= D \cdot B / 2$



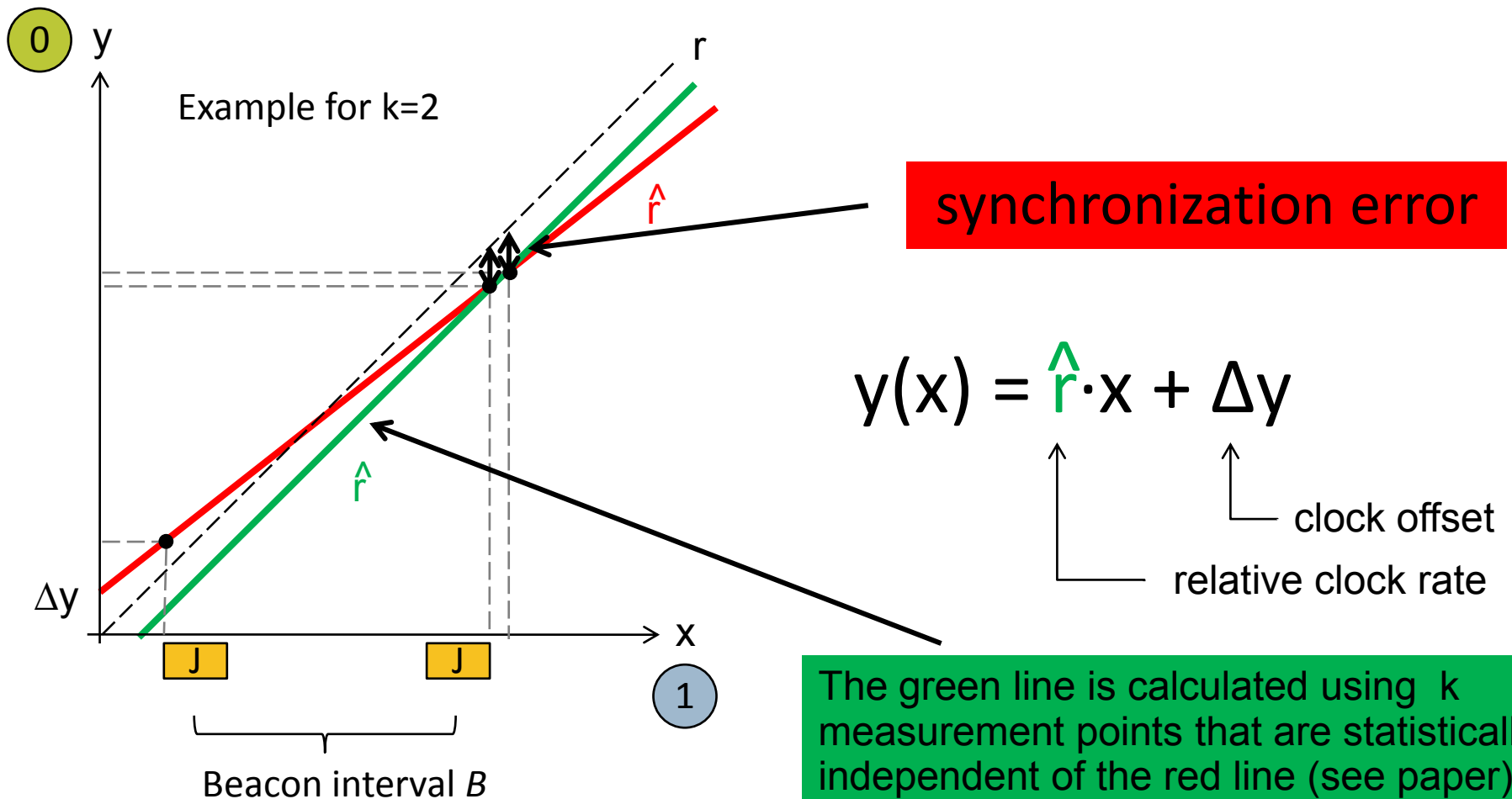
## PulseSync

Expected time  
 $= D \cdot t_{\text{pulse}}$



# The PulseSync Protocol (2)

- Remove self-amplification of synchronization error
  - Fast flooding cannot completely eliminate amplification



# Evaluation

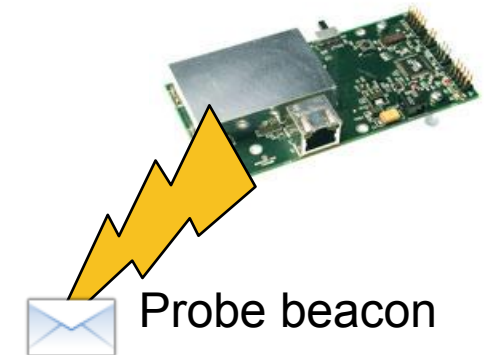
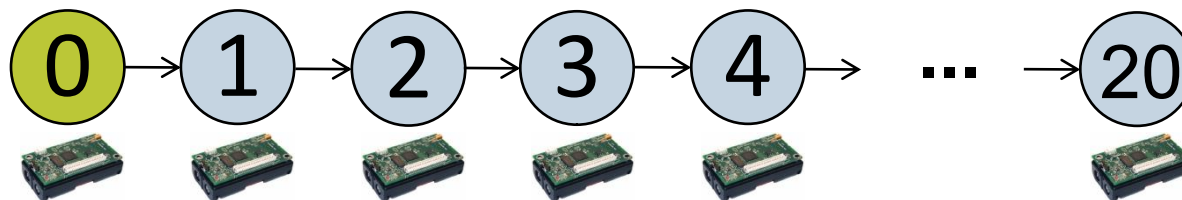
- Testbed setup

- 20 Crossbow Mica2 sensor nodes
- PulseSync implemented in TinyOS 2.1
- FTSP from TinyOS 2.1



- Network topology

- Single-hop setup, basestation
- Virtual network topology (white-list)
- Acknowledgments for time sync beacons

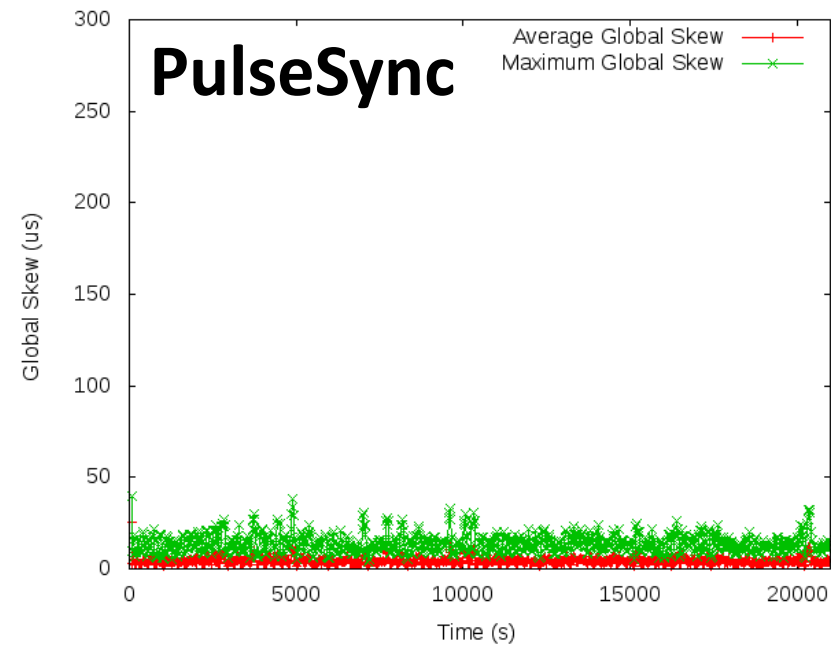
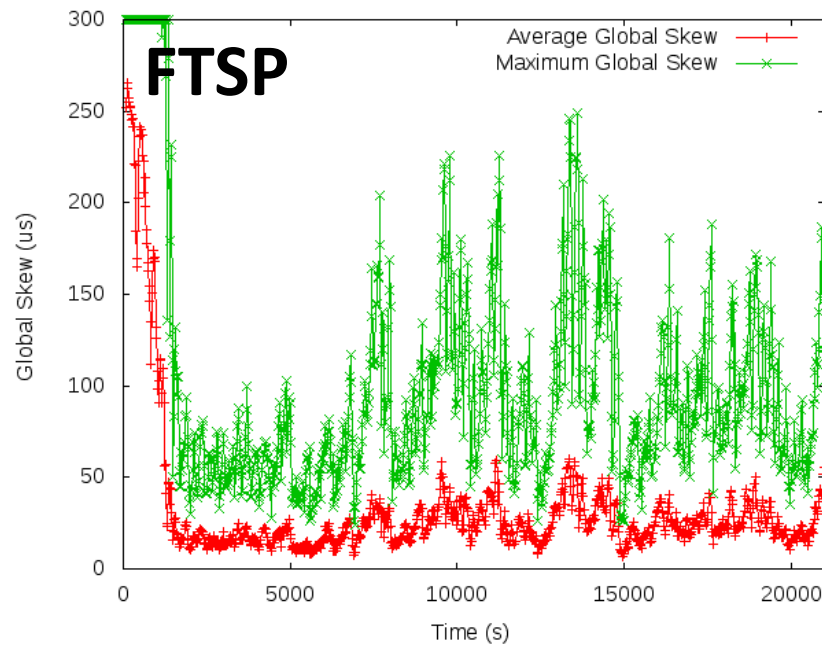




# Experimental Results

## ■ Global Clock Skew

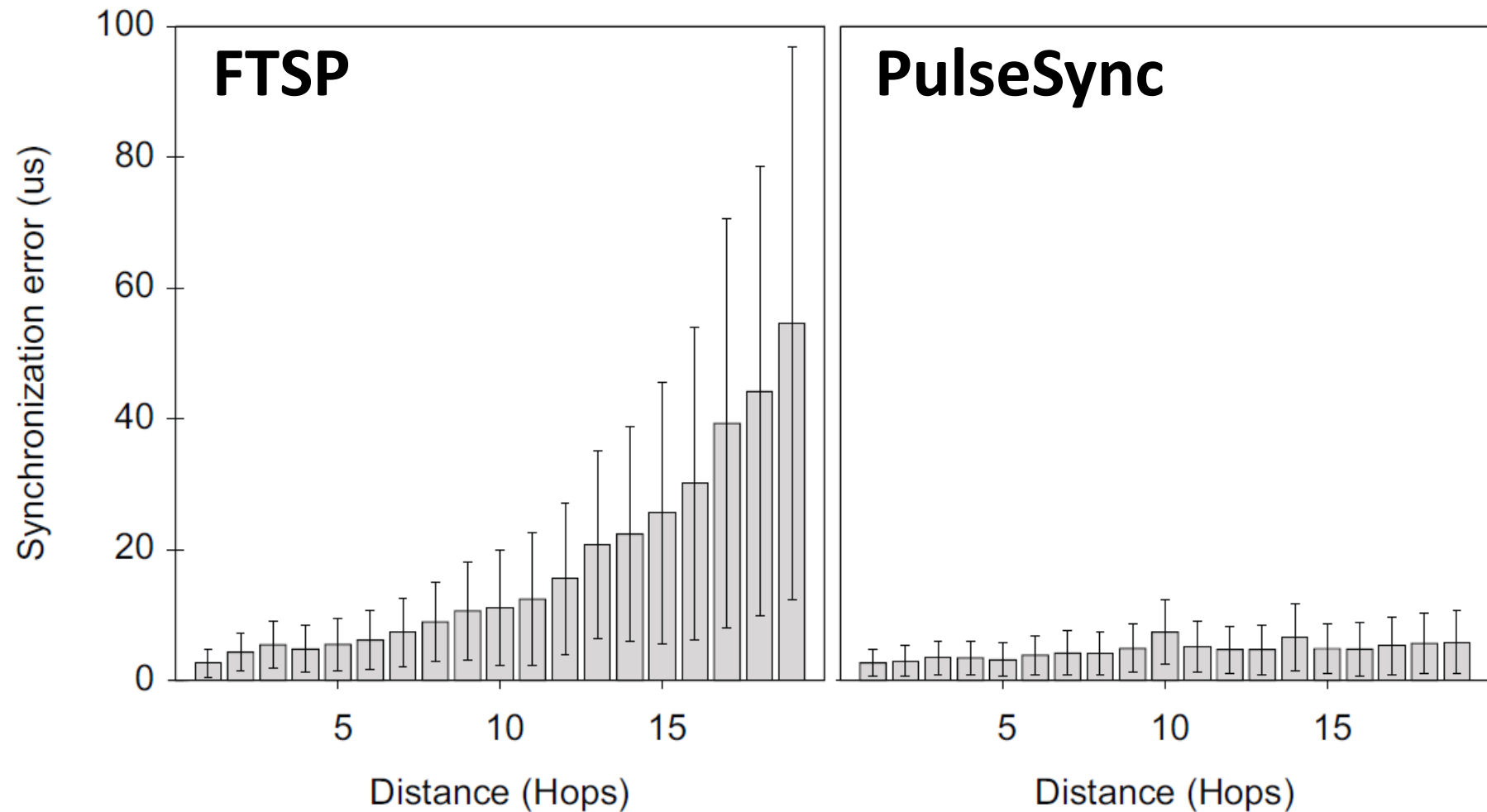
- Maximum synchronization error between any two nodes



Synchronization Error	FTSP	PulseSync
Average (t>2000s)	23.96 $\mu$ s	4.44 $\mu$ s
Maximum (t>2000s)	249 $\mu$ s	38 $\mu$ s

## Experimental Results (2)

- Synchronization Error vs. distance from root node





# Outlook

- Extension to more general network topologies
- Schedule synchronization beacons without collisions
  - Time information has to propagate quickly through the network
  - Avoid loss of synchronization pulses due to collisions

This is known as **wireless broadcasting**,  
a well-studied problem (in theory)

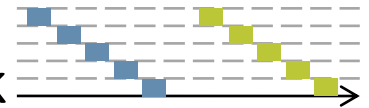
# Conclusions

- Theoretical insights into clock synchronization
  - Lower bound on the global clock skew

$$|\hat{y} - y| \sim \frac{J\sqrt{d}}{\sqrt{k}}$$

- PulseSync: a novel clock synchronization algorithm

- Flooding sync pulses at high speed through the network
- Matches the lower bound on the global skew (shown in the paper)



- Testbed experiments on a 20-node line topology
  - Prototype implementation of PulseSync
  - PulseSync outperforms FTSP for this setting

